ATHARVA EDUCATIONAL TRUST'S
# ATHARVA COLLEGE OF ENGINEERING
(Approved by AICTE, Recognized by Government of Maharashtra
& Affiliated to University of Mumbai - Estd. 1999 - 2000)
ISO 21001:2018 ISO 14001:2015 ISO 9001:2015
NAAC Accredited A+
**ACADEMIC YEAR: 2025-26 (EVEN SEMESTER)**

**Department of Electronics and Telecommunication Engineering**

**ACE/EXTC/FR/22/2025-26**
**DATE: 08/01/2026**

## Report of TE EXTC Mini project orientation 2025-26

| | |
|---|---|
| **Event Name:** | TE EXTC Mini Project Orientation program |
| **College/Institute Name:** | Atharva College of Engineering |
| **Date and Time:** | 8th January at 9 am onwards |
| **Venue:** | Lab 6, EXTC Project lab 4th Floor, Phase 1, ACE |
| **Speaker Name and Designation** | Prof. Shikha Malik, Assistant Professor, EXTC Department ,ACE<br>Prof. Mahalaxmi Palinje, Assistant Professor, EXTC Department ,ACE |
| **Topic of Event:** | TE EXTC Mini Project Orientation program |
| **Mode of conduction:** | Offline mode |
| **Coordinators of the Event:** | Prof. Shikha Malik , Prof. Mahalaxmi Palinje |
| **Targeted Audience:** | TE students of EXTC Dept. |
| **Mapping** | PO1, PO2,PO3,PO5,PO12, PSO1 |
| **Brief Description of Event:** | The report presents the orientation of the subject Mini Project 2B: FPGA Based Project (Course Code: ECM601) prescribed under the University of Mumbai – R2019 C-Scheme for Third Year Electronics and Telecommunication Engineering. The orientation session was conducted on 8th Jan 2026.<br><br>The subject is designed to provide hands-on exposure to FPGA technology, Verilog HDL programming, and complete project implementation flow, thereby bridging the gap between theoretical knowledge and practical VLSI/embedded system applications.<br><br>**Objectives of the Event:**<br><br>● The objective of the Orientation Program was to familiarize students with the **Mini Project 2B: FPGA Based Project (ECM601)** syllabus prescribed by the University of Mumbai.<br><br>● The program aimed to provide clarity on course structure, learning objectives, project expectations, assessment methodology, and industry relevance of FPGA-based design. |



Address: Malad-Marve Road, Charkop Naka, Malad (W), Mumbai 400095, Maharashtra, India

- It also intended to motivate students towards self-learning, teamwork, and hands-on implementation using modern EDA tools and FPGA platforms.

**Topics Covered in the Event:**

The following key topics were covered during the orientation session:

1. Overview of **Mini Project 2B syllabus** and its importance in the TY EXTC curriculum
2. Course objectives, outcomes, and expected skill development
3. Explanation of **module-wise syllabus structure**
4. Project selection guidelines and approval process
5. FPGA-based project implementation steps
6. Use of **EDA tools** such as Xilinx ISE, ModelSim, and simulation workflows
7. Project documentation, logbook maintenance, and project management tools
8. **Assessment scheme**, review process, and evaluation criteria
9. Industry relevance, career opportunities, and research orientation in FPGA/VLSI domain

**Outcome of the Event:**

At the end of the Orientation Program, students were able to:

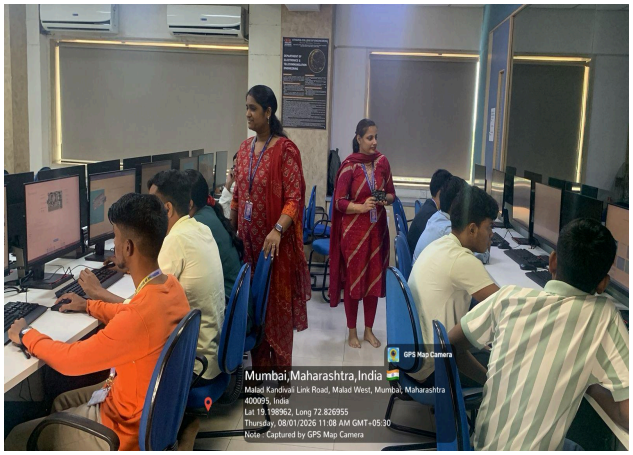
1. Understand the scope and significance of **FPGA-based mini projects**
2. Gain clarity on syllabus structure, modules, and learning roadmap
3. Identify suitable project domains aligned with FPGA applications

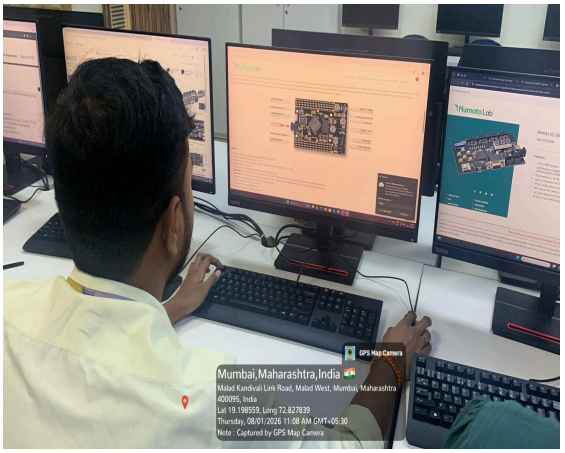


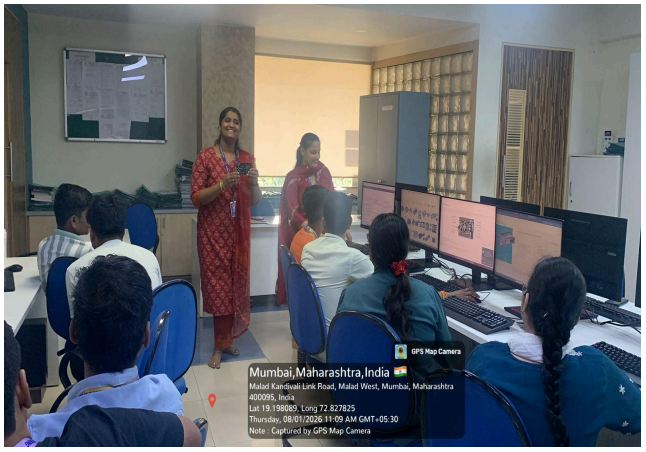
Address: Malad-Marve Road, Charkop Naka, Malad (W), Mumbai 400095, Maharashtra, India

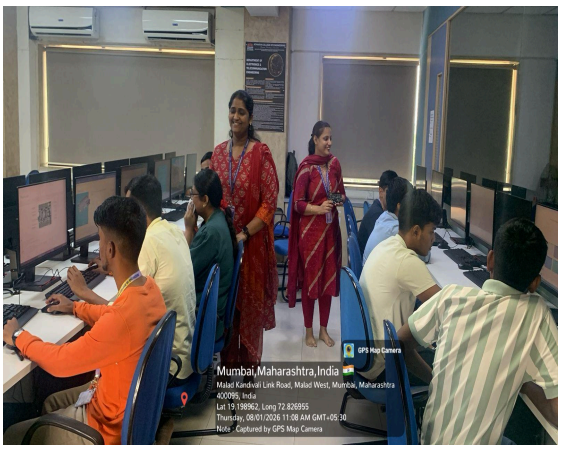
| | |
|---|---|
| | 4. Understand expectations related to project execution, documentation, and evaluation<br>5. Become aware of industry-relevant tools, hardware platforms, and design practices<br>6. Develop motivation towards teamwork, self-learning, and practical implementation<br>7. Relate the subject learning to future careers in **VLSI, FPGA design, and embedded systems** |

**Photographs of the Event:**




Prof. Shikha Malik

EXTC TE Mini Project Coordinator

Prof. Mahalaxmi Palinje

EXTC TE Mini Project Coordinator

Dr. Kiruthika B.

HOD EXTC, ACE

PRINCIPAL
ATHARVA COLLEGE OF ENGINEERING
MUMBAI

Dr. Ramesh Kulkarni

Principal



Address: Malad-Marve Road, Charkop Naka, Malad (W), Mumbai 400095, Maharashtra, India